

**In The Specification**

Please amend the paragraph in lines 4-11 on page 6 as follows:

Referring to Fig. 1, external AAL2 PVCs, AAL2 PVC1 (100)... to AAL2 PVCn (102), are operatively connected to an intermediate node 104 in a wireless access gateway (101). A set of internal AAL2 PVCs, AAL2 PVC1 (106)... to AAL2 PVCn (108), is also operatively connected to the intermediate node 104. Each of the internal AAL2 PVCs is operatively connected thereto a respective transcoder 110... 112. For allocating individual CIDs to transcoder channels on an as needed basis, a packet switch control 114 is operatively connected to the intermediate node 104, the set of internal AAL2 PVCs 106, 108 and the transcoders 110, 112.

Please amend the paragraph in lines 12-20 on page 6 as follows:

Based upon an algorithm that takes into account at least a current state of each of the transcoders 110, 112 and a current load of all of the transcoders, the switch controller 114 instructs the at least one intermediate node to switch individual AAL2 CPS-Packets from the external AAL2 PVCs and to the internal AAL2 PVCs. The set of internal AAL2 PVCs 106, 108 and the set of transcoders 110, 112 form a plurality of DSP channels. The intermediate node 104 switches individual AAL TYPE 2 CPS-packets of a new call, for example, from an external

Oct 05 2009 3:01PM

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AAL2 PVC of the plurality of external AAL2 PVCs 100, 102 to an internal PVC of the set of internal AAL2 PVCs 106, 108.